

**AMENDMENTS TO THE CLAIMS:**

This listing of claims will replace all prior versions, and listings, of claims in the application:

**LISTING OF CLAIMS:**

Claim 1 (Currently Amended) A field emission device (FED) comprising:

a substrate;

a cathode formed over the substrate;

micro-tips having nano-sized surface features, each micro-tip including the nanosized surface features being of a single integral homogeneous material, formed on the cathode;

a gate insulation layer with wells ~~each of~~ in which a single micro-tip is located ~~in~~, the gate insulation layer formed over the substrate;

a gate electrode with gates aligned with the wells such that ~~each of~~ the micro-tips ~~is~~ are exposed through a corresponding gate, the gate electrode formed on the gate insulation layer;

a focus gate insulation layer having openings ~~each of~~ to which one or more gates correspond ~~to~~, the focus gate insulation layer formed on the gate electrode; and

a focus gate electrode with focus gates aligned with the openings of the focus gate insulation layer, the focus gate electrode formed on the focus gate insulation layer.

Claim 2 (Currently Amended) The field emission device of claim 1, wherein a resistor layer is formed over or beneath the cathode, or a resistor layers ~~is~~ are formed over and beneath the cathode.

Claim 3 (Previously Presented) The field emission device of claim 1, wherein the micro-tips having nano-sized surface features comprise a plurality of nano-tips.

Claim 4 (Previously Presented) The field emission device of claim 1, wherein a resistor layer is formed beneath the cathode.

Claim 5 (Previously Presented) The field emission device of claim 1, wherein resistor layers are formed over and beneath the cathode.

Claim 6 (New) The field emission device of claim 1, wherein two or more gates correspond to an opening in the focus gate insulation layer.